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Inventor(s): Kyu-Chan LEE, et al. Conf. No. 4320
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Title: SEMICONDUCTOR MEMORY DEVICE HAVING AN INTERNAL VOLTAGE
GENERATION CIRCUIT FOR SELECTIVELY GENERATING AN INTERNAL
VOLTAGE ACCORDING TO AN EXTERNAL VOLTAGE LEVEL

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

FOREIGN PATENT DOCUMENTS

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<u>VN</u>	<u>—</u>	11-086544	March 30, 1999	Japan	Yamazaki Kyoji

OTHER DOCUMENTS

Exam Init	Ref	Author, Title, Date, Pertinent Pages, Etc.)
<u>VN</u>	<u>—</u>	English Language Abstract of Japanese Publication No. 11-086544

Examiner: V. Nguyen

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